

AMENDMENTS TO THE CLAIMS

This Listing of Claims will replace all prior versions and listings of claims in this application.

Listing of Claims:

1. (Currently Amended) A digital light processing (DLP) [-] projector for the active projection of stereoscopic images, comprising:
 [[a]] at least one digital mirror device (DMD) [(4)],
 at least a first driver circuit interacting with a memory [(6)] and controlling at least one DMD [(4)], [[and]]
 at least a first signal input [(1)] for the input of image data of at least a first image channel and a second image channel, ~~characterized by: At~~
 at least a second driver circuit interacting with a memory [(6')]; and
 a switching device [(7)], which is connected on one side with the two driver circuits and on another side with at least the one DMD [(4)];
 wherein the first driver circuit processes image data of the first image channel and the second driver circuit processes image ~~[[date]]~~ data of the second image channel, and the switching device [(7)] directs to the DMD [(4)], alternately, one or more images or frames from the first driver circuit, and one or more images or frames from the second driver circuit.
2. (Currently Amended) The DLP-projector as claimed in claim 1, further comprising ~~characterized by~~ at least a second signal input [(1')], wherein the first signal input [(1)] receives the image signals of the first image channel and forwards such to the first driver circuit, and the second signal input [(1')] receives the image signals of the second image channel and forwards such to the second driver circuit.
3. (Currently Amended) The DLP-projector as claimed in claim 1 [or 2], further comprising ~~characterized by~~ a clock [(2)] for synchronizing of the image signals.

4. (Currently Amended) The DLP-projector as claimed in claim 3, wherein ~~eharaeterized in~~
~~that~~ the signal inputs ~~(1, 1')~~ are synchronized with one another by a clock signal.

5.-10. (Cancelled)

11. (New) The DLP-projector as claimed in claim 3 or claim 4, wherein the driver circuits are synchronized with one another by a clock signal.

12. (New) The DLP-projector as claimed in claim 1, further comprising a microprocessor, which controls the switching device and/or produces a control signal for shutter-glasses.

13. (New) The DLP-projector as claimed in claim 1, wherein the driver circuits comprise at least one ASIC of type DDP 1000 of Texas Instruments or successor models thereof.

14. (New) The DLP-projector as claimed in claim 1, wherein the switching device connects all signal lines with the first driver circuit and data lines of the DMD only selectively with the first driver circuit or another driver circuit.

15. (New) The DLP-projector as claimed in claim 1, wherein the switching device, for the projection of the image data of an image channel as a mono-image, or a mono-image sequence, connects the data lines of the DMD during a desired period of time only with the corresponding data lines of one of the driver circuits.

16. (New) The DLP-projector as claimed in claim 1, wherein the driver circuits and the switching device are arranged on a circuit board.